# SJC Institute of Technology, SJC

**Department of Computer Science & Engineering**

## Time Table (Autonomy)

**Year: Aug. 2017 - Nov. 2017** **Semester : III A**

**Room No: 302 Branch : CSE**

|  |  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- | --- |
| Time  Days | 9.00 - 10.00 | 10.00 - 10.50 | 11.00-11.50 | 11.50-12.40 | 12.40 -1.30 | 1.30 – 2.20 | 2.20-3.10 | 3.10-4.00 |
| Monday | Maths | DS | DSD Lab | DS | DSD Lab | DS | DSD Lab |  | DSD | DC |  |
| Tuesday | DMS | DC |  | DSD |  | Maths | DS |  |
| Wednesday | DS | DMS | CO | DC |  | DS | DSD Lab | DS | DSD Lab | DS | DSD Lab |
| Thursday | DMS |  | CO | Maths |  |  |  |  |
| Friday | DSD | DC | DSD | CO |  | DS | DMS |  |
| Saturday | CO | DS | Maths |  |  |  | | |

|  |  |  |  |  |
| --- | --- | --- | --- | --- |
| Subject Code | Sub. Expansion | | Staff Name | |
| **16CS301** | **Generating Functions and Transform Techniques Combinatorics** | | **Maths Faculty** | |
| **16CS302** | **Digital System Design** | | **Raghunandan K R** | |
| **16CS303** | **Discrete Mathematical Stuctures & Graph Theory** | | **Uday Kumar Reddy** | |
| **16CS304** | **Computer Organization and Design** | | **Pradeep Nazareth** | |
| **16CS305** | **Data Structures** | | **Sannidhan M S** | |
| **16CS306** | **Data Communication** | | **Uday Kumar Shenoy** | |
| **Lab** | | **Batch** | | **Staff Initial** |
| **Data Structures Lab** | | **A1** | | **SMS** |
| **A2** | | **SMS** |
| **Digital System Design Lab** | | **A1** | | **RKR** |
| **A2** | | **RKR** |

**Class Advisor: Signature of HOD**